

## REMARKS

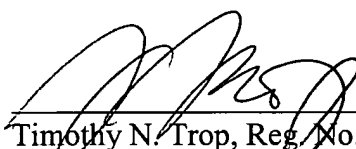
Claim 1 has been amended to call for a memory element holding voltage of at least about .9 volts. The specification at page 8, lines 7-13 supports the added limitation.

The cited Lowrey application gives the combined holding voltage of all three components, two select devices and one memory element in paragraph 124. Since the holding voltage of each select device is one and, if the holding voltage may be determined by adding up the holding voltages of the components, then the holding voltage of the memory element would be .8 volts.

Therefore, reconsideration of the rejection is requested in view of the amendment.

Respectfully submitted,

Date: May 3, 2006



Timothy N. Trop, Reg. No. 28,994  
TROP, PRUNER & HU, P.C.  
8554 Katy Freeway, Ste. 100  
Houston, TX 77024  
713/468-8880 [Phone]  
713/468-8883 [Fax]

Attorneys for Intel Corporation